IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

M. HIRAKI, et al.

Application No.:

Rule 1.53(b) Continuation of U.S. Patent Application

Serial No. 10/379,545, filed March 6, 2003

Filed:

On even date herewith

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, MEMORY MODULE, STORAGE DEVICE AND THE METHOD FOR REPAIRING SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

Art Group of Parent:

2824

Examiner of Parent:

H. N. Nguyen

UNDER 37 CFR §1.97 & 1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 October 15, 2003

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO/SB/08A which list documents cited in parent applications Serial No. 10/379,545, filed March 6, 2003, which is a continuation of application Serial No. 10/178,217 filed June 25, 2002; which is a continuation of application Serial No. 09/989,133, filed November 21,

2001 (now USP 6,449,197); which is a continuation of Serial No. 09/756,233, filed January 9, 2001 (now USP No. 6,324,103) which is a divisional application of Serial No. 09/497,119 filed February 3, 2000 (now USP No. 6,341,090), which is a divisional application of Serial No. 09/435,035, filed November 5, 1999 (now USP No. 6,201,733), the entire disclosures of which are hereby incorporated by reference.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, Deposit Account No. 01-2135 (501.37777CC8).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

Gregory E. Monton

Reg. No. 28,141

GEM/dlt

1300 North Seventeenth Street, Suite 1800

Arlington, Virginia 22209 Telephone: (703) 312-6600

Facsimile: (703) 312-6666

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO STATEMENT BY APPLICANT M. HIRAKI, et al. First Named Inventor Not assigned yet Art Unit **Examiner Name** Not assigned yet (use as many sheets as necessary) Sheet of 1 **Attorney Docket Number** 501.37777CC8

U.S. PATENT DOCUMENTS					
Examiner Initials'	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		5.671.180	09/97	Hiauchi	
		5 694 359	12/97	Park	
		5.812.468	09/98	Shirley	
		6.003.133	12/99	Moughanni et al	·
		· 6.023.761	02/00	Ott	
		161.157	11/92	Owen et al.	
		5.278.839	01/94	Matsumoto et al	
		5.822.256	10/98	Bauer et al	
		5.619.470	4/97	Fukumoto	· · · · · · · · · · · · · · · · · · ·
		5.986.932	11/99	Ratnakumar et al	
	-	6.078.541	.06/00	Kitagawa et al	

		FOREIG	N PATENT DO	CUMENTS		
Examiner Initials'	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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		JP 6-243677	2/9/94			
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